SMT Workshop'07

5th International Workshop on Satisfiability Modulo Theories

(Previously called PDPAR: Pragmatics of Decision Procedures in Automated Reasoning)

Affiliated with CAV'07

Berlin, Germany, 1-2 July 2007

—CALL FOR PAPERS—

Workshop Chairs

Sava Krstić Intel Corporation

Albert Oliveras Techn. Univ. of Catalonia

Invited Speakers

Rupak Majumdar Univ. of California, LA

Peter O'Hearn Queen Mary, Univ. of London

Programme Committee

Clark Barrett New York University

Alessandro Cimatti ITC-Irst, Trento

Byron Cook Microsoft Cambridge

Amit Goel Intel Corporation

Aarti Gupta NEC Labs America

Shuvendu Lahiri Microsoft Redmond

Leonardo de Moura Microsoft Redmond

Robert Nieuwenhuis Techn. Univ. of Catalonia

Silvio Ranise LORIA, Nancy

Roberto Sebastiani Università di Trento

Ofer Strichman Technion

Cesare Tinelli The University of Iowa

Ashish Tiwari SRI International

Background

Deciding the satisfiability of first-order formulas modulo background theories, known as the Satisfiability Modulo Theories (SMT) problem, has proved to be useful in verification, compiler optimization, scheduling, and other areas.

The success of SMT techniques depends on the development of both domain-specific decision procedures for each concrete theory (e.g. linear arithmetic, the theory of arrays, or the theory of bit-vectors) and combination methods that allow one to obtain more versatile SMT tools. These two ingredients together make SMT techniques well-suited for use in larger automated reasoning and formal verification efforts.

Aims and Scope

The aim of the workshop is to bring together researchers and users of SMT tools and techniques. Continuing the PDPAR tradition, we especially encourage submission of papers focused on pragmatic aspects. Relevant topics include but are not limited to:

- New decision procedures and new theories of interest
- Combinations of decision procedures
- Novel implementation techniques
- Benchmarks and evaluation methodologies
- Applications and case studies
- Theoretical results

Important dates

Submission deadline 23 April Notification of acceptance/rejection 21 May Final version due 4 June

Paper submission and Proceedings

- Original papers: contain original research (simultaneous submissions are not allowed) and sufficient detail to assess the merits and relevance of the submission. For papers reporting experimental results, authors are strongly encouraged to make their data available. Given the informal style of the workshop, we will welcome work in progress and novel ideas that are not yet competitive in practice.
- Presentation-only papers: describe work recently published or submitted and will not be included in the proceedings. We see this as a way to provide additional access to important developments that SMT Workshop attendees may be unaware of.

Full submission guidelines and proceedings information are at the workshop web page: http://www.lsi.upc.edu/~oliveras/smt07.

Student travel awards

SMT 2007 will partially reimburse some students for their conference-related expenses. Information on applying can be found at the workshop web page.